



Call for Papers

IEEE Asian Solid-State Circuits Conference (A-SSCC) 2005



Location: Ambassador Hotel, Hsinchu, Taiwan
Date: November 1, 2005 (Tue.) – November 3, 2005 (Thu.)

Sponsored by IEEE SSCS, IEEE region 10 SSCS chapters
In Cooperation with Industrial Technology Research Institute (ITRI)
and Semiconductor Technology Academic Research Center (STARC)
Organized by National Taiwan University, National Chiao Tung University, National Tsing Hua University
and SoC Technology Center (STC/ITRI)

About the Conference

The IEEE Asian Solid-State Circuits Conference (A-SSCC) 2005 is the international forum for advances in solid-state circuits design held in Asia. The conference is supported by the IEEE Solid-State Circuits Society.

Paper Submission

Prospective authors are invited to submit full-length, four-page papers, including figures, tables and references, to the official A-SSCC'05 website. All papers will be handled and reviewed electronically. Papers are solicited in the following categories. Low-power, and/or low-voltage approaches for all the areas are welcome. Measurement results are always recommended, especially for analog/RF circuits. Pure CAD tool papers are not considered in the scope. Dual submission with other conferences is not allowed. A special edition of IEEE Journal of Solid-State Circuits for this conference will be prepared..

1. **Analog:** Analog and mixed-signal circuits including, but not limited to, amplifiers, A/D, D/A converters, etc.
2. **Wireless and Wireline Communications:** Circuits related to RF and wireline communications.
3. **Digital:** Digital circuits and related circuit blocks including, but not limited to, microprocessors, digital signal processors, communication processors, etc.
4. **Memory:** Circuits related to any type of memories.
5. **System-Level Integration:** Circuits and related design methodologies for SiP, SoC, manufacturability, statistical design, packaging, test, interconnections, noise, signal integrity, etc.
6. **Emerging Technologies and Applications:** Circuits related to emerging technologies and applications such as MEMS, sensors, displays, highly scaled technologies, ubiquitous electronics, wireless sensor network, wearable electronics, bio-medical applications, photonics, organic/flexible electronics, etc. Technology papers are also in the scope if circuit implications are abundant.
7. **Industry Program:** This special category accepts only papers based on state-of-the-art products. The paper may cover specifications, applications, state-of-the-art points, chip photos, chip architecture/software, circuits (not necessarily very original, significant improvement is ok), live demo if any, characterization results, and packaging/testing results.
8. **Student Design Contest Program:** Please visit official website below.

Important dates

June 3, 2005, 20:00(GMT)	Paper submission deadline (changed from the first call for papers)
July 15, 2005	Acceptance notification
September 1, 2005	Deadline for final papers submission

Further Information

Visit the A-SSCC official website <http://a-sscc.org/> for further details such as submission guidelines and template. Or contact the conference secretary, Prof. Shyh-Jye (Jerry) Jou at e-mail: jerryjou@mail.nctu.edu.tw, TEL: 886-3-5131475 and Fax: 886-3-5724361 or the TPC vice-chair, Prof. Makoto Ikeda at e-mail: ikeda@silicon.u-tokyo.ac.jp, TEL: 81-3-5841-6661 and Fax: 81-3-5841-8912.

Steering Committee Chair: Chorng-Kuang Wang, National Taiwan University, Taiwan
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